



#16/8-16822
Docket No. 740756-2296

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: PATENT Application of:

Hisashi OHTANI et al.

Serial No. 09/837,552

Filed: April 19, 2001

For: SEMICONDUCTOR DEVICE AND

MANUFACTURING METHOD THEREOF

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) Group Art Unit: 2815

) Examiner: N. Drew Richards

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231, on April 5, 2002.

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference listed on the attached Form PTO-1449 be made of record in the above-identified application.

The reference listed on the attached Form PTO-1449 and its English Language abstract were previously submitted with an Information Disclosure Statement dated August 13, 2001. Applicants submit herewith a full English translation of this reference for the Examiner's consideration.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380 (740756-2296).

Respectfully submitted,

Eric J. Robinson
Registration No. 38,285

NIXON PEABODY LLP
8180 Greensboro Drive, Suite 800
McLean, Virginia 22102
(703) 790-9110